

Fig.1 Overview of a Printed Circuit Board Product Life Cycle

1/6

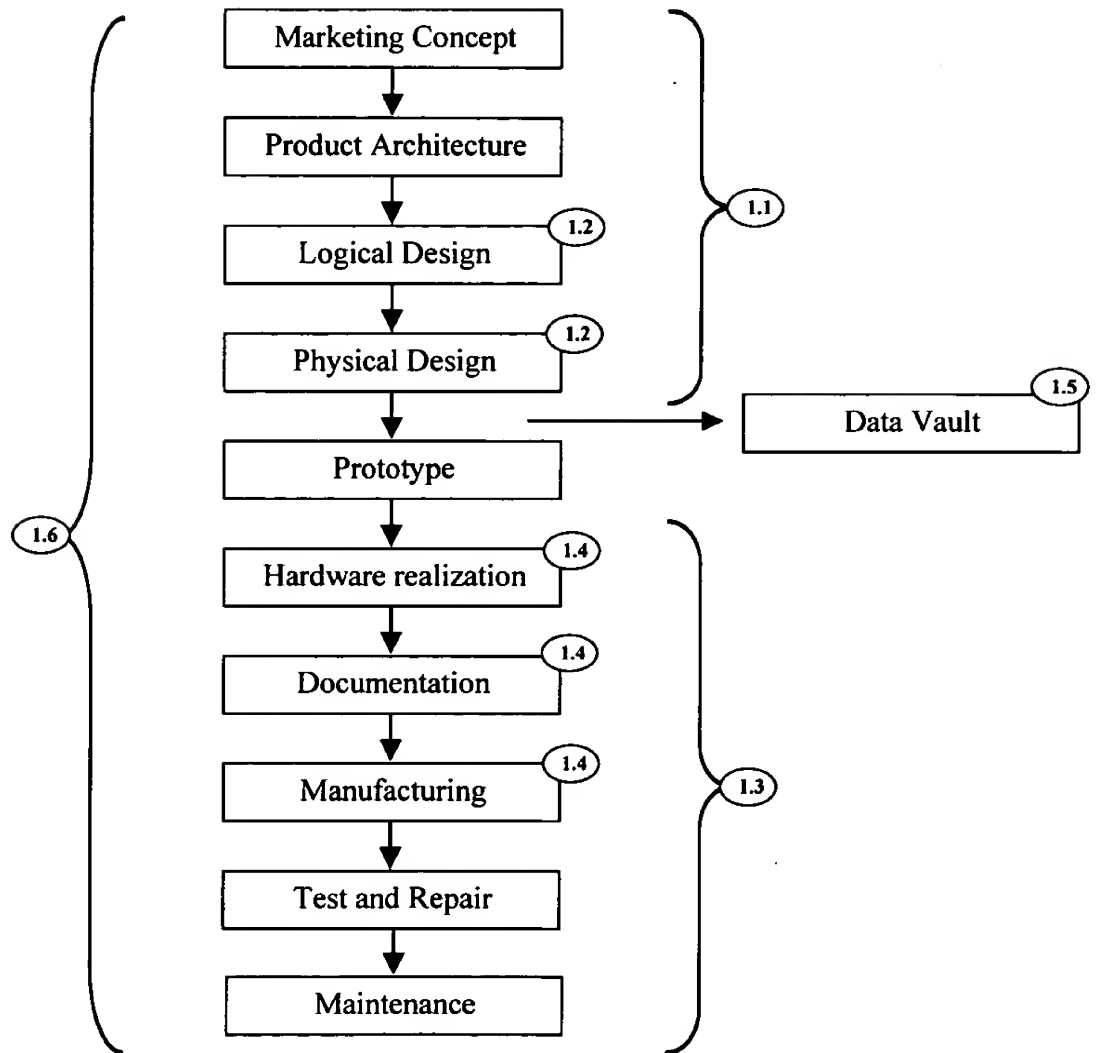


Fig.2 Hardware Packaging Hierarchy of an Electronic Product

2/6

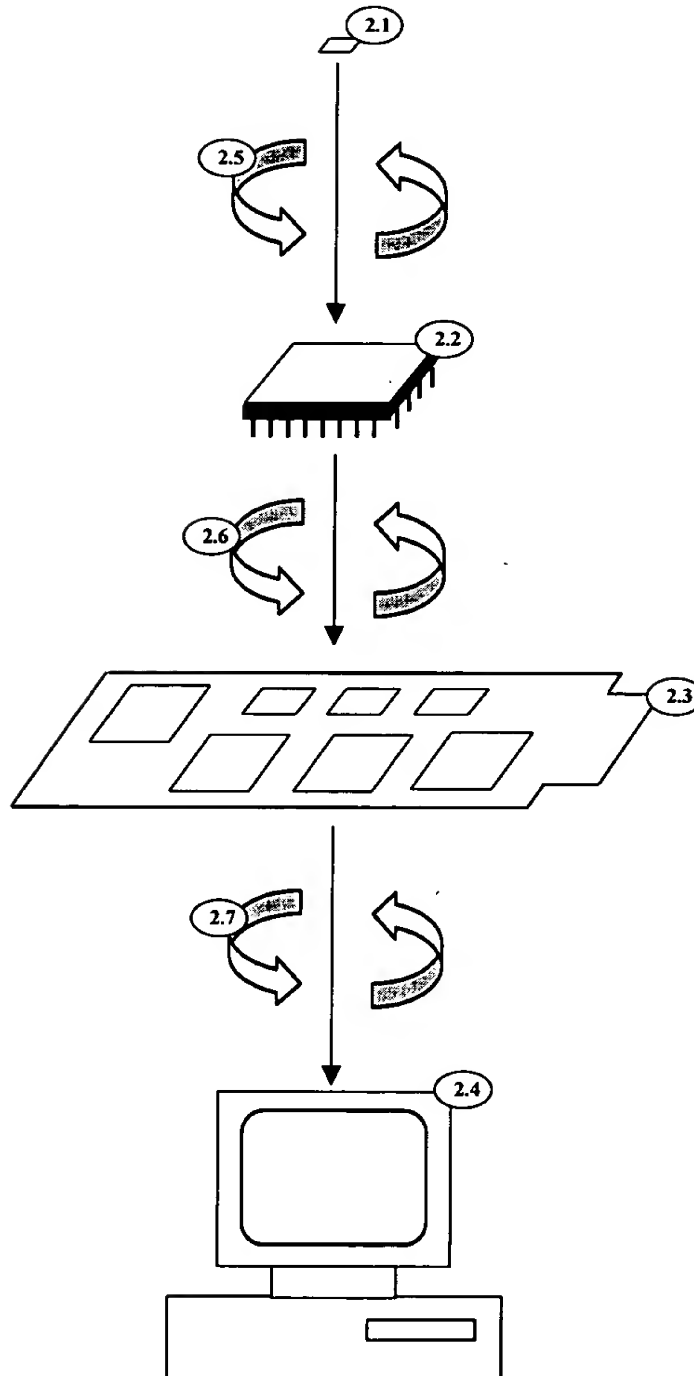


Fig.3A Architecture of the Tool

3/6

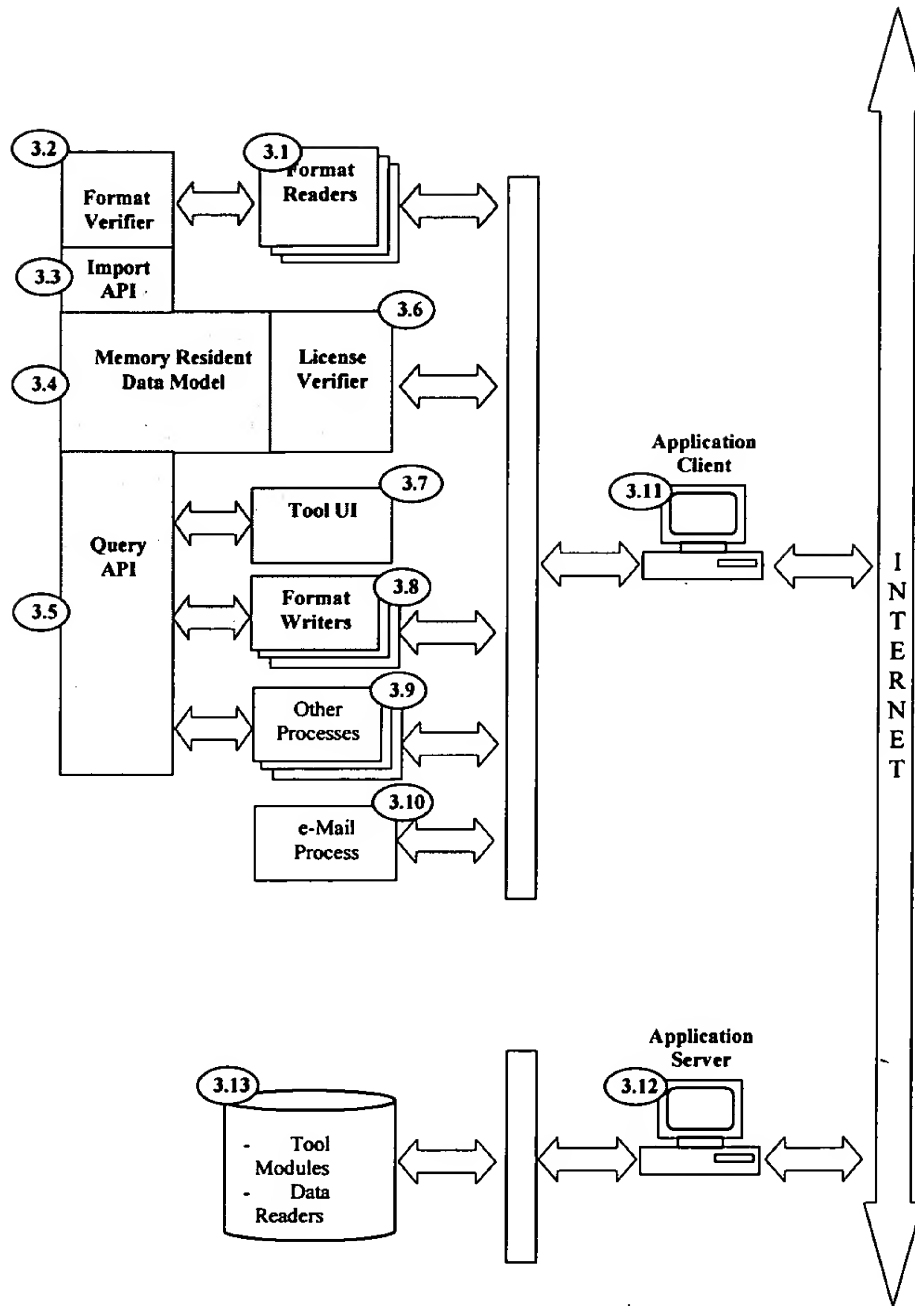


Fig.3B Architecture of the Tool

4/6

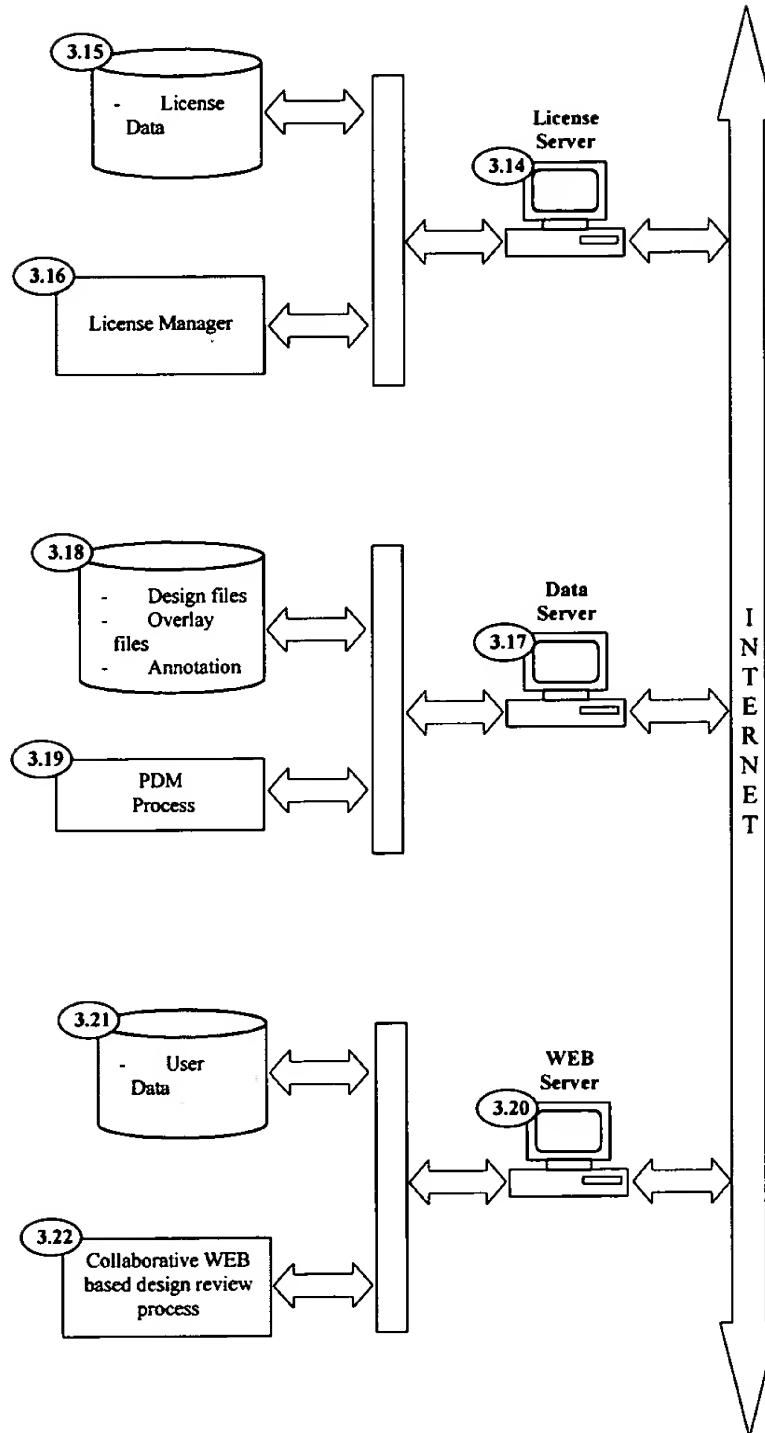


Fig.4A Tool Process Flow

5/6

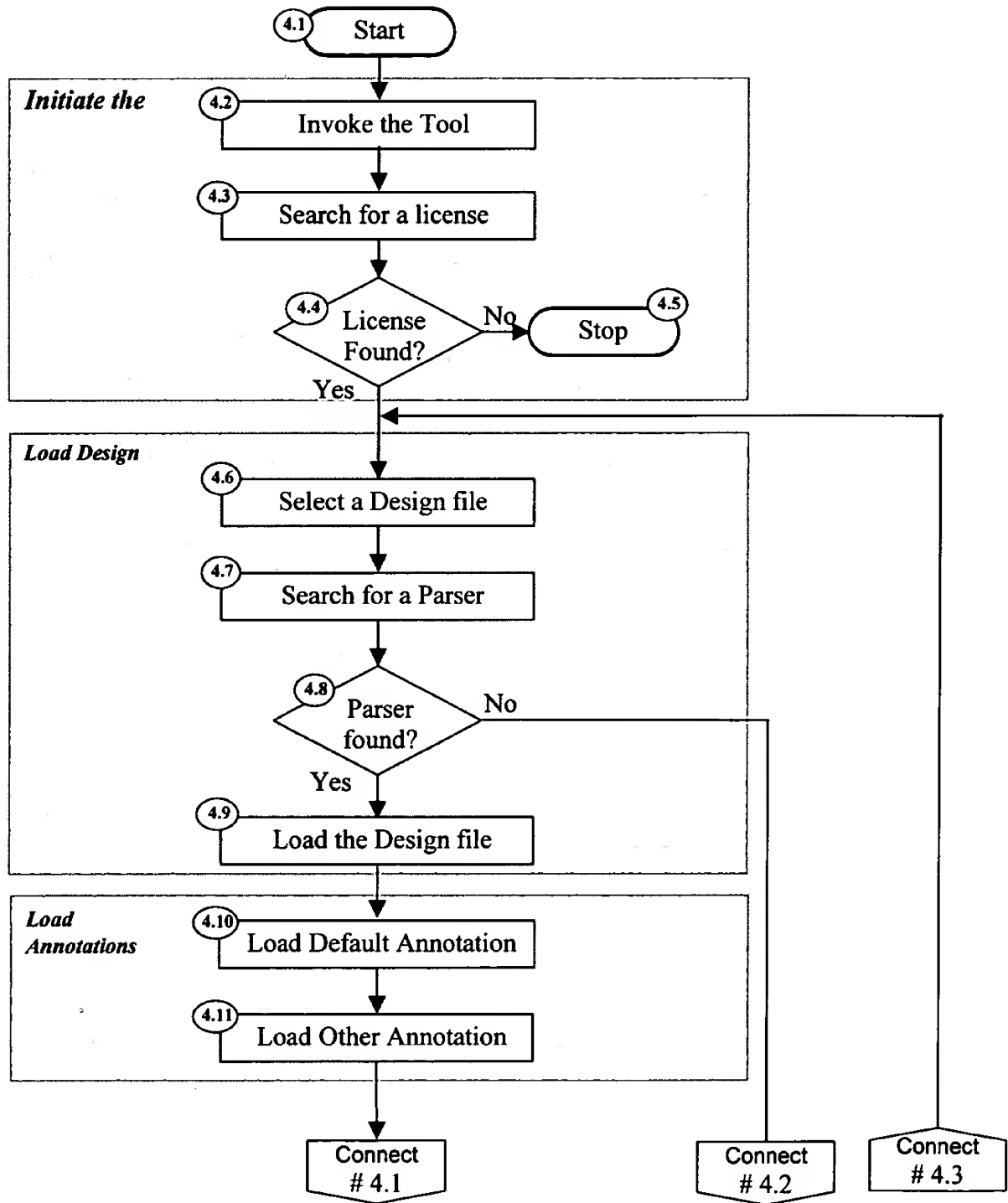


Fig.4B Tool Process Flow  
6/6

